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Docket No.: M4065.0624/P624
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard E. Rhodes

Application No.: 10/751,439

Confirmation No.: 3917

Filed: January 6, 2004

Art Unit: 2818

For: METHOD AND APPARATUS PROVIDING
CMOS IMAGER DEVICE PIXEL WITH
TRANSISTOR HAVING LOWER
THRESHOLD VOLTAGE THAN OTHER
IMAGER DEVICE TRANSISTORS

Examiner: L. K. Tran

RESPONSE TO RESTRICTION REQUIREMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed August 12, 2005 (Paper No. 81205), Applicant elects Group I (claims 1-79) for continued examination, without traverse.

Allowance of the application is respectfully solicited.

Dated: August 24, 2005

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Gabriela I. Coman

Registration No.: 50,515

DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant